

Amendments to the Specification:

Please amend paragraphs [00018] and [00019] as follows:

Sub B17
[00018] The transmitter portion of the transceiver 120 consists of an integrated voltage controlled oscillator (VCO) 122, a complete fully programmable direct digital synthesizer 124, and a power amplifier 126. The receiver portion consists of a low-noise amplifier 128, mixer 130, IF ~~amplifier~~ amplifiers 132, limiter, FM/FSK demodulator 134 with an external LC tank circuit 136, and a data slicer 138.

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[00019] The demodulator 134 may be used for analog (FM) and digital (FSK) frequency demodulation. The data slicer 138 preferably acts as a comparator. The data slicer 138 provides binary logic level signals, derived from the demodulated and low pass-filtered IF signal, that are able to drive external CMOS compatible inputs in the microcontroller 110. The noninverting input is directly connected to an internal reference voltage and the inverting input is driven by the output of the low-pass filter amplifier/post detection amplifier. The decision threshold of the data slicer ~~128~~ 138 is determined by the internal reference voltage.